

1. (Twice Amended)

A power chip resistor comprising:

- a first and second film resistor each having (a) a substrate with a top surface, a bottom surface, a first end surface, an opposing end surface, a first side surface and an opposing side surface, (b) a film resistive element on the top surface of each substrate, (c) an end cap on the first end surface and electrically connected to the film resistive element, and (d) a second end cap on the opposing end surface and electrically connected to the film resistive element;
- the second film resistor of approximately the same physical size as the first film resistor, the second film resistor of approximately the same orientation as the first film resistor;
- an encapsulant between the top surface of the first film resistor and the bottom surface of the second film resistor;
- a first nickel barrier connecting the end cap on the first end surface of the first film resistor and the first end surface of the second film resistor and mechanically bonding the film resistors without adhesive;
- a second nickel barrier connecting the second end cap on the second end surface of the first film resistor and the second end cap on the second end surface of the second film resistor and mechanically bonding the film resistors without adhesive.

7. (Twice Amended)

The power chip resistor of claim 1 further comprising:

- a third film resistor having (a) a substrate with a top surface, a bottom surface, a first end surface, an opposing end surface, a first side surface and an opposing side surface, (b) a film resistive element on the top surface of the substrate, (c) an end cap on the first end surface electrically connected to the film resistive element, and (d) a second end cap on the opposing end surface and electrically connected to the film resistive element; a second encapsulant between the top surface of the substrate of the second film resistor and the bottom surface of the substrate of the third film resistor, the first nickel barrier electrically connected to the end cap of the first end surface of the third film resistor, the second nickel barrier electrically connected to the second end cap on the second end surface of the third film resistor.

8. (Twice Amended)

The power chip resistor of claim 7 further comprising:

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a fourth film resistor having (a) a substrate with a top surface, a bottom surface, a first end surface, an opposing end surface, a first side surface and an opposing side surface, (b) a film resistive element on the top surface of the substrate, (c) an end cap on the first end surface electrically connected to the film resistive element, and (d) a second end cap on the opposing end surface and electrically connected to the film resistive element; a third encapsulant between the top surface of the substrate of the third film resistor and the bottom surface of the substrate of the fourth film resistor, the first nickel barrier electrically connected to the end cap of the first end surface of the fourth film resistor, the second nickel barrier electrically connected to the second end cap on the second end surface of the fourth film resistor.

9. (Twice Amended)

A power chip resistor comprising:

a first and second film resistor each having (a) a substrate with a top surface, a bottom surface, a first end surface, an opposing end surface, a first side surface and an opposing side surface, (b) a film resistive element on the top surface of each substrate, (c) an end cap on the first end surface of each substrate electrically connected to the film resistive element, and (d) a second end cap on the opposing end surface of each substrate and electrically connected to the film resistive element; a glass encapsulant between the top surface of the substrate of the first film resistor and the bottom surface of the substrate of the second film resistor;

a first metal barrier covering the end caps on the first end surface of the substrate of the first and second film resistors and mechanically bonding the film resistors without adhesive;

a second metal barrier covering the second end caps on the opposing end surface of the substrate of the first and second film resistors and mechanically bonding the film resistors without adhesive.

16. (Twice Amended)

The power chip resistor of claim 9 further comprising:

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- a third film resistor having (a) a substrate with a top surface, a bottom surface, a first end surface, an opposing end surface, a first side surface and an opposing side surface, (b) a film resistive element on the top surface of the substrate, (c) an end cap on the first end surface electrically connected to the film resistive element, and (d) a second end cap on the opposing end surface and electrically connected to the film resistive element;
- a second encapsulant between the top surface of the substrate of the second film resistor and the bottom surface of the substrate of the third film resistor, the first nickel barrier electrically connected to the end cap of the first end surface of the third film resistor, the second nickel barrier electrically connected to the second end cap on the second end surface of the third film resistor.

17. (Twice Amended)

The power chip resistor of claim 16 further comprising:

- a fourth film resistor having: (a) a substrate with a top surface, a bottom surface, a first end surface, an opposing end surface, a first side surface and an opposing side surface, (b) a film resistive element on the top surface of the substrate, (c) an end cap on the first end surface electrically connected to the film resistive element, and (d) a second end cap on the opposing end surface and electrically connected to the film resistive element; a third encapsulant between the top surface of the substrate of the third film resistor and the bottom surface of the substrate of the fourth film resistor, the first nickel barrier electrically connected to the end cap of the first end surface of the fourth film resistor, the second nickel barrier electrically connected to the second end cap on the second end surface of the fourth film resistor.

18. (Twice Amended)

A stacked chip resistor comprising:

- a first chip resistor and a second chip resistor, each chip resistor having a substrate with a thick film resistive element attached to the substrate, a first end cap and a second end cap, each end cap being an electrical terminal connected to the thick film resistive element;
- a layer of glass placed between the first chip resistor and the second chip resistor;
- a first nickel barrier, the nickel barrier electrically connecting the first end cap of the first chip resistor and the first end cap of the second chip resistor;
- a second nickel barrier, the nickel barrier electrically connecting the second end cap of the first chip resistor and the second end cap of the second chip resistor;
- the nickel barriers bonding the chip resistors without adhesive.

24. (Twice Amended)

The stacked chip resistor of claim 18 further comprising:

- a third chip resistor, the third chip resistor having a substrate with a thick film resistive element attached to the substrate, a first end cap and a second end cap, each end cap being an electrical terminal connected to the thick film resistive element,;
- a second layer of glass placed between the second chip resistor and the third chip resistor, the first nickel barrier electrically connected to the first end cap of the third chip resistor, the second nickel barrier electrically connected to the second end cap of the third chip resistor.

25. (Twice Amended)

The stacked chip resistor of claim 24 further comprising:

- a fourth chip resistor, the fourth chip resistor having a substrate with a thick film resistive element attached to the substrate, a first end cap and a second end cap, each end cap being an electrical terminal connected to the thick film resistive element, the second chip resistor, and the third chip resistor;
- a third layer of glass placed between the third chip resistor and the fourth chip resistor, the first nickel barrier electrically connecting the first end cap of the fourth chip resistor with the first end cap of the first chip resistor and the first end cap of the second chip resistor and

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the first end cap of the third chip resistor, the second nickel barrier electrically connected to the second end cap of the fourth chip resistor.